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# Experimental study of multiple-shot unitary channels discrimination using the IBM Q computers

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- Tomáš Bezděk
- Paulina Lewandowska
- Ryszard Kukulski

### What is Quantum Channel Discrimination?





- Core Idea: Given a quantum device (a black-box), it applies either channel  $\Phi_0$  or  $\Phi_1$ .
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  - Certification and benchmarking QC

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  - Certification and benchmarking QC Jałowiecki, K., Lewandowska, P., Pawela, Ł. (2023). PyQBench: A Python library for benchmarking gate-based quantum computers. SoftwareX, 24, 101558.
- Earliest theoretical work: single shot state discrimination, solved by Helstrom in 1969.
- Recent practical experiments: **Distinguishing unitary gates on IBM Q** by S. Liu.

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  - Unitary vs. general CPTP channels
  - Parallel, sequential, and adaptive (multi-shot) strategies

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- Key variations and modifications:
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  - Parallel, sequential, and adaptive (multi-shot) strategies
- Our main motivation is to test the performance of various theoretically explored schemes on real hardware such as IBM Q devices.

#### Notion of quantum state, channel and measurement

General quantum states are  $\rho \in PSD(\mathcal{X})$  such that  $Tr\rho = 1$ , we denote them  $\Omega(\mathcal{X})$ .

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All linear maps  $\Phi : L(\mathcal{X}) \to L(\mathcal{Y})$  with property:

Φ ⊗ 1<sub>L(Z)</sub> : PSD(X ⊗ Z) → PSD(Y ⊗ Z) are called completely positive (CP).
 Tr(Φ(X)) = Tr(X) are called trace preserving (TP).

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Such CPTP maps are called **quantum channels** and we denote them  $C(\mathcal{X}, \mathcal{Y})$ .

• A Positive Operator-Valued Measure (POVM)  $\mathcal{P}$  is a collection of operators  $\{E_0, \dots, E_n\} \subset \mathsf{PSD}(\mathcal{X})$  with property  $\sum_{i=0}^n E_i = \mathbb{1}$ . The probability of obtaining  $E_i$  after measuring  $\rho$  is given by Born rule  $\mathsf{Tr}(E_i\rho)$ .

#### Numerical range, arc function and diamond norm

For any operator  $X \in L(\mathcal{X})$ , the **numerical range** is defined as:

$$\mathsf{W}(X) \coloneqq \{ \langle x | X | x \rangle : | x \rangle \in \mathcal{X}, \langle x | x \rangle = 1 \}.$$

It is always a convex set (Hausdorff–Toeplitz theorem). For normal X, W(X) equals the convex hull of the eigenvalues.

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For unitary  $U \in L(\mathcal{X})$ , the **arc function**  $\theta(U)$  captures the minimal arc on the unit circle containing spec(U):

$$\theta(U) \coloneqq \min \Big\{ \varDelta \in [0, 2\pi) : \exists \, \alpha \text{ s.t. } \mathsf{spec}(U) \subset \{ e^{i\theta} : \theta \in [\alpha, \alpha + \varDelta] \} \Big\}.$$

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• The diamond norm  $\|\cdot\|_{\diamond}$  for  $\Phi: L(\mathcal{X}) \to L(\mathcal{Y})$  is:

$$\|\Phi\|_{\diamond} = \left\|\Phi \otimes \mathbb{1}_{\mathsf{L}(\mathcal{X})}\right\|_{1},$$

where  $\|\Phi\|_1 = \max\{\|\Phi(X)\|_1 : X \in \mathsf{L}(\mathcal{X}), \mathsf{Tr}(X^{\dagger}X) \leq 1\}$ 

#### Mathematical preliminaries

#### Probability of successful channel discrimination

- We are given black-box quantum channel  $\Phi$  which is either  $\Phi_0$  or  $\Phi_1$  ( $\in C(\mathcal{X}, \mathcal{Y})$ ).
- To determine which was applied, we prepare an input state  $\rho \in \Omega(\mathcal{X} \otimes \mathcal{Z})$  and perform a binary measurement  $\{E_0, E_1\} \subset \mathsf{PSD}(\mathcal{Y} \otimes \mathcal{Z})$  after applying  $\Phi \otimes \mathbb{1}_{\mathsf{L}(\mathcal{Z})}$ .



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- The success probability derived from Born's rule:

$$\begin{split} p_{\mathrm{succ}} = & \frac{1}{2} \mathrm{Tr} \big( E_0(\varPhi_0 \otimes \mathbb{1}_{\mathsf{L}(\mathcal{Z})})(\rho) \big) \\ & + \frac{1}{2} \mathrm{Tr} \big( E_1(\varPhi_1 \otimes \mathbb{1}_{\mathsf{L}(\mathcal{Z})})(\rho) \big). \end{split}$$

By the Holevo-Helstrom theorem,  $p_{succ}$  can be expressed using the diamond norm:

$$p_{\mathrm{succ}} = \frac{1}{2} + \frac{1}{4} \left\| \varPhi_0 - \varPhi_1 \right\|_\diamond.$$

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• The goal is to choose  $\rho$  and  $\{E_0, E_1\}$  to maximize  $p_{succ}$ .

#### Single-shot discrimination of unitary channels

- **Perfect discrimination** is situation whenever  $p_{succ} = 1$ .
- Unitary channels are maps  $\Phi_U \in C(\mathcal{X})$  for which  $\Phi_U(X) = UXU^{\dagger}$  where  $U \in L(\mathcal{X})$  is the unitary matrix

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- For unitary channels  $\Phi_U$  and  $\Phi_V$ , we can show:

$$\|\Phi_U - \Phi_V\|_{\diamond} = 2\sqrt{1-\nu^2}, \quad \nu = \min_{w \in W(V^{\dagger}U)} |w|$$

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- Perfect discrimination  $0 \in W(V^{\dagger}U) \Leftrightarrow \exists |\psi\rangle$  such that  $\langle \psi | V^{\dagger}U | \psi \rangle = 0$ .
- At the same time  $0 \in W(V^{\dagger}U) \iff \theta(V^{\dagger}U) \geq \pi$
- Equivalence:  $\theta(V^{\dagger}U) \ge \pi \Leftrightarrow 0 \in W(V^{\dagger}U) \Leftrightarrow \nu = 0 \Leftrightarrow \|\Phi_U \Phi_V\|_{\diamond} = 2 \Leftrightarrow p_{\mathsf{succ}} = 1$

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- The **parallel strategy**: apply  $U^{\otimes N}$  and compare with  $V^{\otimes N}$ .
- Arc function scales:

$$\theta((V^{\otimes N})^{\dagger}U^{\otimes N}) = N\theta(V^{\dagger}U), \text{ if } N\theta(V^{\dagger}U) < 2\pi$$

- $\blacksquare$  Perfect discrimination  $\Leftrightarrow N\theta(V^{\dagger}U) \geq \pi$
- ⇒ Copies required:

$$N \geq \left\lceil \frac{\pi}{\theta(V^{\dagger}U)} \right\rceil$$

• This guarantees  $0 \in W((V^{\otimes N})^{\dagger}U^{\otimes N}) \Rightarrow$  diamond norm = 2  $\Rightarrow p_{succ} = 1$ 



- Assume for simplicity  $\theta(V^{\dagger}U) = \pi/N$ , where  $N = w \cdot d$ .
- In our setup N copies of the black box  $\Phi$  can be composed in the rectangle hybrid scheme,
- where w is number of qubits and each qubit the unknown operation is composed d times.

#### Discrimination of unitary channels

#### Multi-shot Discrimination of Unitary Channels



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$$\Phi^{\otimes w} \circ \Phi_{X_{d-1}} \circ \Phi^{\otimes w} \circ \dots \circ \Phi_{X_1} \circ \Phi^{\otimes w}$$

where X<sub>1</sub>,..., X<sub>d-1</sub> are arbitrary unitary matrices for mid-circuit processing.
For some U and V processing is not necessary (but optimal always exists). Then

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- Strategy types:
  - w = N, d = 1: Parallel
  - w = 1, d = N: Sequential
  - General w, d: Hybrid rectangular

#### Discrimination of Unitary Channels on IBM Q

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- Circuit is divided into:

1 Discriminator – prepares state

$$|\psi\rangle = \frac{1}{\sqrt{2}} (|0\rangle^{\otimes w} + \alpha |1\rangle^{\otimes w})$$

2 Unknown gate(s) - for example identity or RZ(\$\phi\$)
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- **2** Unknown gate(s) for example identity or  $RZ(\phi)$
- **3** Measurement unitary transformation and measurement in comp. basis
- Two discriminator and measurement implementations:
  - Standard CNOT-based GHZ construction (simple, but high transpilation overhead)
  - Optimized ECR-based version for Eagle R3 (lower gate count, better fidelity)

#### Example 1.



 $\blacksquare$  We will distinguish between identity  $\varPhi_1$  and  $\varPhi_{\mathsf{RZ}(\phi)}$  for

$$\mathsf{RZ}(\phi) = \begin{pmatrix} e^{-i\frac{\phi}{2}} & 0\\ 0 & e^{i\frac{\phi}{2}} \end{pmatrix}$$

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$$|\psi\rangle = \frac{1}{\sqrt{2}} \left(|0\cdots 0\rangle + \lambda |1\cdots 1\rangle\right) \in \mathbb{C}^{2^{w}}$$

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• Measurement is then  $E_0 = |\psi_0\rangle\langle\psi_0|$  and  $E_1 = \mathbb{1} - E_0$ , where  $|\psi_0\rangle = |\psi\rangle$  after applying  $\Phi_1$ 

#### Discriminator Implementations: CNOT vs. ECR

**Goal:** Prepare a maximally entangled GHZ-like state on w qubits:

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We implemented two types of discriminator circuits to generate this:



Figure: 6 qubit CNOT-based GHZ discriminator



Figure: 6 qubit ECR-based discriminator optimized for Eagle R3

#### Measurement Methods: Short Measurement

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- CNOT-based: For the identity channel, outputs are consistently all zeros. For  $\Phi_{\mathsf{RZ}(\theta)}$ , a single bit is flipped (e.g., 001000).
- ECR-based: Yields more complex disjoint bitstring sets

(e.g.,  $\{001111, ..., 010001\}$  vs.  $\{110001, ..., 100101\}$ ).

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Figure: CNOT-based short measurement



Figure: ECR-based short measurement

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- More robust to bit-flip noise, but higher circuit depth increases decoherence risk.

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#### Performance of implementations: 6-Qubit System

 Setup: 6-qubit experiments comparing four transpilation strategies using Short and XOR measurement circuits.

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- Setup: 6-qubit experiments comparing four transpilation strategies using Short and XOR measurement circuits.
- Key Insights:
  - XOR measurement slightly outperforms short measurement when using ECR + Transpiler strategy.
  - Manual fixed mapping offers marginal benefits in this small system size.

Measurement	Short	XOR
CNOT + Transpiler	88.8%	86.4%
ECR + Transpiler	83.8%	90.0%
ECR + Transpiler + Fixed Map	84.4%	85.3%
ECR + Fixed Map (No Opt.)	83.3%	85.6%

Table: Accuracy of different transpilation strategies for discrimination scheme on 6-qubits obtained on IBM Brisbane, using short and XOR measurement schemes. Each circuit was executed with 10,000 shots. Ambiguous measurement outcomes were randomly assigned.

#### Performance of implementations: 11-Qubit System

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# Performance of implementations: 11-Qubit System

- **Setup:** 11-qubit experiments with five transpilation strategies.
- Key Insights:
  - **Short measurement**: Best result from the default ECR + Transpiler.
  - **XOR measurement** greatly benefits from topology-aware design and fixed-qubit mapping, achieving up to **71.8% accuracy**.

Measurement Transpilation Strategy	Short	XOR
CNOT + Transpiler	43.3%	48.5%
ECR + Transpiler	55.0%	54.5%
ECR (Topol.) + Transpiler	36.1%	47.2%
ECR (Topol.) + Transpiler + Fixed Map	32.0%	71.5%
ECR (Topol.) + Fixed Map (No Opt.)	33.4%	71.8%

Table: Accuracy of different transpilation strategies for discrimination scheme on 11-qubit obtained on IBM Brisbane using short and XOR measurement schemes. Each circuit was executed with 10,000 shots. Ambiguous measurement outcomes were randomly assigned.

#### Impact of Circuit Structure: Sequential vs Parallel

- Comparison between **purely sequential** and **purely parallel** discrimination protocols.
- **Goal:** Distinguish between the identity and  $R_z(\pi/N)$  gates using Short or XOR measurement schemes.



Figure: (Left) Sequential scheme. (Right) Parallel scheme. Solid blue: Short; dashed red: XOR.

#### Impact of Circuit Structure: Sequential vs Parallel

- Comparison between **purely sequential** and **purely parallel** discrimination protocols.
- **Goal:** Distinguish between the identity and  $R_z(\pi/N)$  gates using Short or XOR measurement schemes.
- Key Observation:
  - Parallel circuits suffer more from noise due to entangling gates.
  - Sequential circuits, though deeper, maintain higher accuracy on real hardware.



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# Impact of Entanglement: Hybrid Schemes

• Hybrid schemes blend sequential and parallel discrimination by fixing total unitary applications N while varying the width of the scheme (qubits).



Figure: (Left) Hybrid scheme for N = 240. (Right) Hybrid scheme for N = 1200. Solid blue: short; dashed red: XOR.

#### Impact of Entanglement: Hybrid Schemes

- Hybrid schemes blend sequential and parallel discrimination by fixing total unitary applications N while varying the width of the scheme (qubits).
- Key Observation:
  - Increased entanglement (circuit width) leads to significantly higher error rates.
  - Confirms that **multi-qubit gate noise** is the dominant error source.



#### Post-Processing Correction of Bit-Flip Errors

- Anomalous behavior: On IBM Brisbane, circuits with specific number of qubits exhibit correlated bit-flip measurement errors across all qubits.
- Correction: Post-processing by swapping expected labels when the success probability drops below 0.5 restores accuracy.



Figure: (Left) Corrected Short. (Right) Corrected XOR. Blue: original, Red: corrected.

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- Anomalous behavior: On IBM Brisbane, circuits with specific number of qubits exhibit correlated bit-flip measurement errors across all qubits.
- Correction: Post-processing by swapping expected labels when the success probability drops below 0.5 restores accuracy.
- Verification: Identical transpiled circuits executed on simulator produce perfect discrimination and no such error is observed.



#### Example 2.



• We will distinguish between  $\Phi_U$  for  $U = \sqrt{X} \operatorname{RZ}(\frac{-\pi}{2N})\sqrt{X}$  and  $\Phi_V$  for  $V = \sqrt{X} \operatorname{RZ}(\frac{\pi}{2N})\sqrt{X}$ , where

$$\sqrt{X} = \frac{1}{2} \begin{pmatrix} 1+i & 1-i \\ 1-i & 1+i \end{pmatrix}$$

• **Observation:**  $\sqrt{X} X \sqrt{X} = 1$ , hence  $U X U X = \sqrt{X} RZ(-\frac{\pi}{N}) \sqrt{X}$ .

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- **Observation:**  $\sqrt{X} X \sqrt{X} = 1$ , hence  $U X U X = \sqrt{X} RZ(-\frac{\pi}{N}) \sqrt{X}$ .
- We use hardware-friendly mid-processing  $X_i = X^{\otimes w}$ , and for convince also pre-processing unitary operation  $X_0 = (X\sqrt{X})^{\otimes w}$  and post-processing operation  $X_d = (\sqrt{X}X)^{\otimes w}$ .
- In this way, Example 2. is equivalent to Example 1.

# Performance of hybrid schemes for Example 2.



Figure: (a) Hybrid scheme forFigure: (b) Hybrid scheme forFigure: (c) Hybrid scheme forN = 4.N = 16.N = 32.

Probability of successful discrimination by hybrid rectangular scheme using the short measurement on IBM Q processor Brisbane. The blue line corresponds to no mitigation. The red line is after error mitigation using MThree package.

Results

#### Results

# Performance of hybrid schemes for Example 2.



Figure: (a) Hybrid scheme for N = 64.

Figure: (b) Hybrid scheme for N = 96.

Figure: (c) Hybrid scheme for N = 1024.

Probability of successful discrimination by hybrid rectangular scheme using the short measurement on IBM Q processor Brisbane. The blue line corresponds to no mitigation. The red line is after error mitigation using MThree package.

- We have studied the discrimination of two quantum unitary channels and benchmarked various schemes for perfect discrimination between them.
- Transpilation:
  - Manual mapping helped on the 11-qubit layout with XOR-based measurement.
  - The overall benefit is often outweighed by the complexity and time consumption of manual mapping.

#### Practical Insight:

- Circuit geometries beyond square layouts may offer a more accurate reflection of the capabilities of the device
- Purely parallel schemes typically perform poorly.
- Purely sequential schemes work well only for a small number of copies.

#### Platform Anomaly:

- Systematic bit-flip errors with 5+ qubits in Example 1.
- Probably hardware/software issue, not circuit design.

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# Thank you for your attention

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